

Electronic Acknowledgement Receipt

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Title of Invention:	RELAXED SIGE PLATFORM FOR HIGH SPEED CMOS ELECTRONICS AND HIGH SPEED ANALOG CIRCUITS
First Named Inventor/Applicant Name:	Eugene A. Fitzgerald
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	ASC-049C1IssueFee.pdf	109013 2689f285f319a1f94d4ff03d3a7a43e437f8b 9e4	no	1

Warnings:

Information:

2	Fee Worksheet (PTO-06)	ASC-049C1Fee.pdf	84989 ba16fd99d93e7ed4bbb7b3e88baaf3b3a99 e6d6e	no	1
Warnings:					
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Total Files Size (in bytes):			194002		
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